

Technical Training 2010

DSP & Communication Theory Short Courses

DSP Theory Algorithms & Architectures	4 Day	May 10-13 November 22-25 December 7-10	Scotland, UK Scotland, UK Munich, DE
Adaptive Filtering & Linear Algebra DSP	3 Day	February 23-25 September 27-29 on request	Scotland, UK Scotland, UK Munich, DE
Digital Communications	3 Day	March 9-11 October 5-7 on request	Scotland, UK Scotland, UK Munich, DE
Synchronisation for Digital Receivers	2 Day	April 29-31 September 1-3 October 1-3 on request	Scotland, UK Scotland, UK Scotland, UK Munich, DE
OFDM for Wireless Communications	3 Day	March 22-24 November 1-3 on request	Scotland, UK Scotland, UK Munich, DE
MIMO for Wireless Communications	2 Day	April 20-21 October 26-27 on request	Scotland, UK Scotland, UK Munich, DE
DSP for FPGAs	4 Day	April 12-15 May 4-7 November 16-19 Nov 29 - Dec 2	Scotland, UK Munich, DE Munich, DE Scotland, UK
FPGAs and Embedded Processors	3 Day	June 8-10 October 12-14	Scotland, UK Munich, DE

Wireless Standards Short Courses

3GPP LTE Physical Layer	3 Day	February 9-11 September 14-16 on request	Scotland, UK Scotland, UK Munich, DE
3GPP UMTS FDD Physical Layer	2 Day	on request	Munich, DE Scotland, UK
DVB-H	1 Day	March 25 November 4 on request	Scotland, UK Scotland, UK Munich, DE
802.16 Physical Layer	2 Day	on request	Munich, DE Scotland, UK

DSP Implementation Short Course

VHDL Simulation and Synthesis	3 Day	May 17-19 on request	Scotland, UK Munich, DE
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Automotive Electronics

MOST Forum	1 Day	March 23	Frankfurt, DE
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Course Information

Course Targets

The intensive technical courses will present and examine the theory and use of algorithms, applications and architectures. The courses will feature the software design flow from concept, to bit true simulation, to actual hardware implementation.

Level

The level of the courses will include some initial review and revision. The style of the course and the hands-on labs sessions mean that this work is self-paced. There will be a number of experienced staff assisting Prof. Stewart during lab sessions making the course suitable for both new graduates and experienced engineers.

Course Presentation

DSP is often seen as an esoteric and very mathematical subject. In these courses, the necessary mathematical theory is presented on a "need to know basis" and in an intuitive style using both simulations and demonstrations. This presentation style and ethos has been presented with considerable success to many companies, both small and large, in both Europe and the USA.

Course Format

50% Lectures and presentation
40% Workshop with software hands-on simulation
10% Tutorial Discussion

Course Materials

All attendees will receive a comprehensive set of electronic and printed versions of the teaching materials. A DVD containing all the simulation models used during the course will also be distributed. The notes provided form a superset of the materials presented on the course and will allow further in depth study after the course.

Instructors

The courses will be led by the team of experienced design engineers from Steepest Ascent of Professor Bob Stewart. They have been successfully presented in Europe, United States and Asia. Prof Bob Stewart has extensive experience presenting industry DSP courses in the USA and Europe. He is currently a faculty member of the Department of Electronic and Electrical Engineering at the University of Strathclyde. Prior to joining the University of Strathclyde, Prof Stewart was a visiting professor in Dept of Electrical Engineering at the University of Minnesota in 1990, and a visiting scholar at the University of Southern California in 1986/7. Since 1997 he has been a part-time visiting professor at UCLA.

Services

Following services are included in the fee for the technical courses: participation at the course/workshop, catering during coffee breaks, soft drinks, lunch, and course documentation. The lectures are given in English.

The course hours are from 9.00 a.m. to 5.00 p.m. for 3 day courses and for 4 day courses it is from 9.00 a.m. to 6.00 p.m. for the first 3 days and until 3 p.m. on the fourth day. The Munich course location is:

DERAG HOTEL and LIVING
Hotel Prinzessin Elisabeth
Geyerstrasse 52, 80469 München, Germany
Phone: +49 89 72017 153, Fax: +49 89 72017 160
<http://www.deraghotels.de/en/PE.htm>

The hotel has reserved a number of rooms. Please make your booking directly with the hotel referring to "qaqadu event gmbh". Further hotels are available through www.hrs.com.

On-site Courses

All short courses can also be offered on-site at companies in either a general or a customised form. If you have specific requirements please do not hesitate to contact us.

Registration

This written registration is effective. The number of participants is limited. The invoice will be mailed with the registration confirmation. Registration deadline is ten workdays before course begin:

- via mail to qaqadu event gmbh, Maximilianstrasse 8, DE-82319 Starnberg – or –
- via fax to: +49-8151-55 50 09 10 – or –
- via E-Mail to contact@hightech-events.com

Terms and Conditions: Invoices have to be settled for participation. For a written cancellation within six weeks before course starts a fee of 200 € plus VAT per person is due. A deregistration within two weeks before the course will cost 50 % of registration fee plus VAT per person. For non-attendance or late notice the whole fee plus VAT per person will be charged. A substitute of the registered participant will be accepted. qaqadu event gmbh reserves the right to cancel or modify the course and place at short notice and will not accept liability for costs incurred by participants or their organisations for cancelled travel arrangements and/or accommodation reservations. All fees exclude German state value added tax.

Herewith I bindingly register for the following course. I accept the terms and conditions.

4 Day Courses	<input type="checkbox"/> € 1.980,00 regular fee
DSP for FPGAs	<input type="checkbox"/> € 1.790,00 early registration until 70 days before course
<input type="checkbox"/> May 4-7, 2010 <input type="checkbox"/> November 16-19, 2010	<input type="checkbox"/> € 1.690,00 per participant of two from the same company
DSP Theory Algorithms & Architectures	<input type="checkbox"/> € 1.590,00 per participant of three or more from the same company
<input type="checkbox"/> December 7-10, 2010	<input type="checkbox"/> € 1.090,00 University Rate (Please enclose evidence.)
3 Day Courses	<input type="checkbox"/> € 1.670,00 regular fee
FPGAs and Embedded Processors	<input type="checkbox"/> € 1.500,00 early registration until 70 days before course
<input type="checkbox"/> October 12-14, 2010	<input type="checkbox"/> € 1.420,00 per participant of two from the same company
MOST Forum	<input type="checkbox"/> € 169,00 early registration by December 31, 2009
March 23, 2010	<input type="checkbox"/> € 249,00 regular fee
	<input type="checkbox"/> Members of the MOST Cooperation: Free
	<input type="checkbox"/> € 49,00 University faculty and students (Please enclose evidence.)
	<input type="checkbox"/> Speakers, members of the program committee and press: Free

Please inform us on the _____ course(s) in Scotland, UK and the US.

We are interested in the _____ course(s) for on-site realization at our premises of _____ (company) in _____ (town, country).

Name _____

Company _____

VAT-ID-No. (for non-German EU only) _____

Title / Position / Department _____

Street / Mail box _____

Country-Zip Code-Town _____

Telephone _____ Fax _____

E-mail _____

Credit Card No. _____ Expiration Date _____ / _____

Town, Date Signature _____